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S/N 10/050621

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	CHENG et al.	Examiner:	Unknown
Serial No.:	10/050621	Group Art Unit:	3729
Filed:	15 January 2002	Docket No.:	8688.258US01
Title:	METHOD OF FABRICATING A MULTI-LAYER CIRCUIT BOARD ASSEMBLY		

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this Transmittal Letter and the paper, as described herein, are being deposited in the United States Postal Service, as first class mail, with sufficient postage, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 2 July 2002.

By:

REQUEST FOR CORRECTED FILING RECEIPT

Office of Initial Patent Examination's Customer Service Center
Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Enclosed is a photocopy of the filing receipt from the United States Patent and Trademark Office in the above-identified application showing requested corrections.

The title is incorrect.

Please replace "METHOD OF FABRICATING A MULTI-LAYER" with --METHOD OF FABRICATING A MULTI-LAYER CIRCUIT BOARD ASSEMBLY--.

Correction of the records of the United States Patent and Trademark Office and issuance of a corrected filing receipt are respectfully solicited.

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AUG - 6 2002

TECHNOLOGY CENTER R3700

Dated: 2 July 2002

MDS/kjr

Respectfully submitted,

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By

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UNITED STATES PATENT AND TRADEMARK OFFICE



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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/050,621	01/15/2002	3729	740	8688.258US01	3	5	1

MOS

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CONFIRMATION NO. 6455

FILING RECEIPT



OC00000007466564

Date Mailed: 02/13/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Yu-Chiang Cheng, Taipei City, TAIWAN;
Jong-Kuei Chen, Hua-Lien Hsien, TAIWAN;

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Assignment For Published Patent Application

Mitac international Corp., Hsinchu Hsien, TAIWAN;

TECHNOLOGY CENTER R3700

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 02/12/2002

Projected Publication Date: 07/17/2003

Non-Publication Request: No

Early Publication Request: No

Title

Method of fabricating a multi-layer circuit board assembly

Preliminary Class

029

81

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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